

Tim Lindquist

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Project Portfolio: <http://timlindquist.me>

EDUCATION

Iowa State University

Master of Science, Electrical Engineering

December 2018

- Cumulative GPA: 4.0

Bachelor of Science, Electrical Engineering

Conferred: December 2017

Magna Cum Laude

- Cumulative GPA: 3.87
- Class Rank: 20/247

DISTINCTIONS

- Awarded “Best Hardware Hack” at HackISU MLH competition *Spring 2016 & 2018*
- Voted “Best Computer Vision Project” in computational perception course *Spring 2018*
- “Best Innovative Idea” for ISU’s Pappajohn’s Entrepreneurial Pitch Contest *Spring 2016*
- Eta Kappa Nu (*IEEE honors society*) / Tau Beta Pi (*Engineering honors society*) *Spring 2017*

WORK EXPERIENCE

Texas Instruments, Digital Design Internship

Summer 2018

- Developed a utility for running design simulations across device platforms, enabling quick debug
- Experience in mixed signal design flow, embedded processor verification, and System Verilog

Micron, Non-Volatile Product Engineer Internship

Summer 2017

- Created statistical software to automate bench data analysis, providing consistency for future results
- Built tool to locate where Vt distributions diverge, useful for easily detecting run to run variations
- Exposed to silicon processes, die assembly, wafer probing, and package testing

IBM, Power Subsystem Qualification Engineer Internship

Summer 2016

- Collaborated with suppliers on early product development, qualification, failure analysis
- Wrote automation script for query data uploads, eliminating months of backlogs
- Constructed a testing rig to increase VRM FA throughput (4x)

Graduate Teachers Assistant

Fall 2017 - present

Dr. Joseph Zambreno & Dr. Thomas Daniels

- Grade performance and identify shortcomings for 200+ senior EE/CPRE/SE students

PROJECTS / RESEARCH

Graduate / Undergraduate Research

Spring 2017 – Spring 2018

Dr. Randall Geiger

- Characterize dielectric properties of a new material for the development of a hydroscopic sensor
- Present findings to VP of R&D at APC Inc. as well as suggested design revisions

Dr. Liang Dong

- Assisted graduate researchers in the development of a nitrate sensing system
- Constructed hardware interfaces and designed housing layout for prototypes

SUBLEQ Processor

Fall 2016

- Implemented the first layout implementation of a single instruction set processor
- My contribution was a 20,000 transistors 256x8 SRAM module used for data/instruction storage

Nintimdo RP

Summer 2017

- Hardware + software solution for shortcomings in Raspberry Pi RetroPie portable systems
- Reached #32 trending on YouTube. Featured articles in: Gizmodo, The Verge, Popular Mechanics

TECHNICAL SKILLS

Programming: C, Assembly, Java, C++, Python, MATLAB, Perl, JSL, Verilog, VHDL, Shell Scripting, OpenCV
Hardware: Power Supplies, Oscilloscopes, Function Generators, FPGA, Spectrum Analyzers, Embedded Processors
Software: Cadence Virtuoso & SimVision, Eagle PCB, UNIX CLI, Modelsim, PSpice, LabView, Autodesk CAD